

JOINT ELECTRON DEVICE ENGINEERING COUNCIL



JEDEC Type Administration Section
EIA Standards Laboratory
32 Green Street
Newark, 2, New Jersey

Announcement

of

Electron Device Type Reregistration

Release No. 2763B (Tentative)

March 26, 1962



The Joint Electron Device Engineering Council announced the proposed reregistration of the following electron device designation

3DG4

on March 21, 1960, in Release No. 2763, under the sponsorship of General Electric Company, Schenectady, New York.

Due to industry objection, this proposed reregistration is not to be considered "final" until further notice.

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EIA Standards Laboratory
32 Green Street
Newark, 2, New Jersey

Announcement

of

Electron Device Type Reregistration

Release No. 2763B (Tentative) *

February 26, 1962



The Joint Electron Device Engineering Council announced the registration of the following electron device designation

3DG4

on March 21, 1960, Release No. 2763, under the sponsorship of General Electric Company, Schenectady, New York.

The sponsor now proposes reregistration based on the following data:

CHARACTERISTICS AND TYPICAL OPERATION

As Registered

	<u>Capacitor-Input Filter</u>	
Full-Wave Rectifier		
AC Plate-Supply Voltage per Plate, RMS	275	Volts
Filter Input Capacitor	40	Microfarads
Total Plate-Supply Resistance per Plate	32	Ohms
DC Output Current	350	Milliamperes
DC Output Voltage at Filter Input	300	Volts
Tube Voltage Drop		
I _b = 350 Milliamperes DC per Plate	25	Volts

As Proposed

	<u>Capacitor-Input Filter</u>	
Full-Wave Rectifier		
AC Plate-Supply Voltage per Plate, RMS	275	Volts
Filter Input Capacitor	40	Microfarads
Total Plate-Supply Resistance per Plate	32	Ohms
DC Output Current	340	Milliamperes
DC Output Voltage at Filter Input	288	Volts
Tube Voltage Drop		
I _b = 350 Milliamperes DC per Plate	32	Volts

* Unless valid objection to this reregistration is lodged with the EIA Standards Laboratory prior to March 26, 1962, this reregistration will be made and this information will be considered "FINAL" WITHOUT FURTHER NOTICE!

JOINT ELECTRON DEVICE ENGINEERING COUNCIL



JEDEC Type Administration Section
EIA Standards Laboratory
32 Green Street
Newark, 2, New Jersey

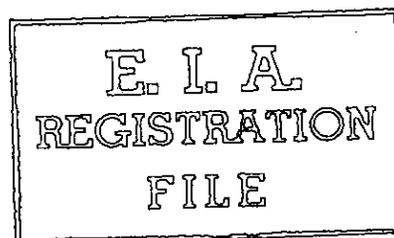
CANCELLATION

of

Electron Device Type Reregistration

Release No. 2763A

January 22, 1962



The Joint Electron Device Engineering Council announced the proposed reregistration of the electron device designation

3DG4

on January 1, 1962, in Release No. 2763A.

This proposed reregistration is hereby cancelled due to industry objection.

JOINT ELECTRON DEVICE ENGINEERING COUNCIL



JEDEC Type Administration Section
EIA Standards Laboratory
32 Green Street
Newark, 2, New Jersey

Announcement
of
Electron Device Type Reregistration
Release No. 2763A (Tentative)
January 8, 1962



The Joint Electron Device Engineering Council announced the proposed reregistration of the following electron device designation

3DG4

on January 1, 1962, in Release No. 2763A, under the sponsorship of General Electric Company, Schenectady, New York.

Due to industry objection, this proposed reregistration is not to be considered "final" until further notice.

JOINT ELECTRON DEVICE ENGINEERING COUNCIL



JEDEC Type Administration Section
EIA Standards Laboratory
32 Green Street
Newark, 2, New Jersey

Announcement

of

Electron Device Type Reregistration

Release No. 2763A (Tentative) *

January 1, 1962



The Joint Electron Device Engineering Council announced the registration of the following electron device designation

3DG4

on March 21, 1960, Release No. 2763, under the sponsorship of General Electric Company, Schenectady, New York.

The sponsor now proposes reregistration based on the attached addendum sheet.

* Unless valid objection to this reregistration is lodged with the EIA Standards Laboratory prior to February 1, 1962, this reregistration will be made and this information will be considered "FINAL" WITHOUT FURTHER NOTICE!

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Happy New Year

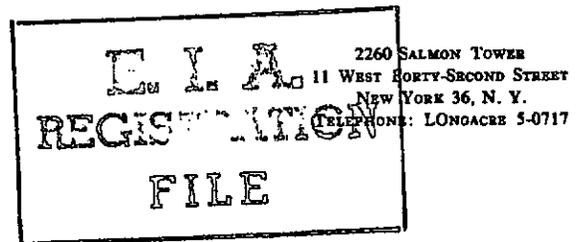
CHARACTERISTICS AND TYPICAL OPERATIONAs Registered

Full-Wave Rectifier	<u>Capacitor- Input Filter</u>	
AC Plate-Supply Voltage per Plate, RMS	275	Volts
Filter Input Capacitor	40	Microfarads
Total Plate-Supply Resistance per Plate	32	Ohms
DC Output Current	350	Milliamperes
DC Output Voltage at Filter Input	300	Volts
Tube Voltage Drop		
I _b = 350 Milliamperes DC per Plate	25	Volts

As Proposed

Full-Wave Rectifier	<u>Capacitor- Input Filter</u>	
AC Plate-Supply Voltage per Plate, RMS	275	Volts
Filter Input Capacitor	40	Microfarads
Total Plate-Supply Resistance per Plate	32	Ohms
DC Output Current	340	Milliamperes
DC Output Voltage at Filter Input	288	Volts
Tube Voltage Drop		
I _b = 350 Milliamperes DC per Plate	32	Volts

JOINT ELECTRON DEVICE ENGINEERING COUNCIL



Announcement
of
Electron Device Type Registration

Release No. 2763

March 21, 1960

The Joint Electron Device Engineering Council announces the registration of the following electron device designation

3DG4

according to the ratings and characteristics found on the attached data sheets on the application of

General Electric Tubes
Schenectady, New York



3DG4 TWIN DIODE

3DG4
ET-T1594
Page 1
2-60

FOR FULL-WAVE POWER RECTIFIER APPLICATIONS

DESCRIPTION AND RATING

The 3DG4 is a twin diode with directly heated cathode designed for use as a full-wave rectifier in the power supply of television receivers.

GENERAL

ELECTRICAL

Cathode—Coated Directly Heated

Cathode-Heating Voltage, AC or DC.....3.3 ± 10% Volts

Cathode-Heating Current.....3.8 Amperes

MECHANICAL

Mounting Position—Any

Envelope—T-12, Glass

Base—Short Medium-Shell Octal 5-Pin

MAXIMUM RATINGS

RECTIFIER SERVICE—DESIGN-MAXIMUM VALUES

Peak Inverse Plate Voltage.....1050 Volts

AC Plate-Supply Voltage per Plate—See Rating Chart I

Steady-State Peak Plate Current per Plate.....1200 Milliamperes

Transient Peak Plate Current per Plate,

Maximum Duration 0.2 Second.....6.5 Amperes

DC Output Current—See Rating Chart I

Bulb Temperature at Hottest Point.....200 C

Design-Maximum ratings are limiting values of operating and environmental conditions applicable to a bogey tube of a specified type as defined by its published data, and should not be exceeded under the worst probable conditions.

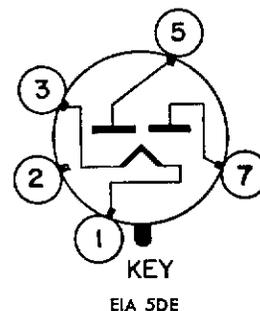
These values are chosen by the tube manufacturer to provide acceptable serviceability of the tube, taking responsibility for the effects of changes in operating conditions due to variations in the characteristics of the tube under consideration.

The equipment manufacturer should design so that initially and throughout life no design-maximum value for the intended service is exceeded with a bogey tube under the worst probable operating conditions with respect to supply-voltage variation, equipment component variation, variation in characteristics of all other tubes in the equipment, equipment control adjustment, load variation, signal variation, and environmental conditions.

The tubes and arrangements disclosed herein may be covered by patents of General Electric Company or others. Neither the disclosure of any information herein nor the sale of tubes by General Electric Company conveys any license under patent claims covering combinations of tubes with other devices or elements. In the absence of an express written agreement to the contrary, General Electric Company assumes no liability for patent infringement arising out of any use of the tubes with other devices or elements by any purchaser of tubes or others.

GENERAL  ELECTRIC

BASING DIAGRAM

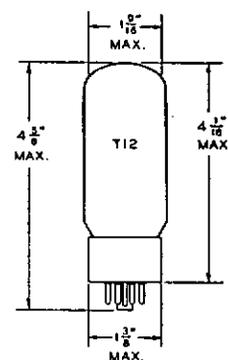


TERMINAL CONNECTIONS

- Pin 1—Cathode*
- Pin 2—Internal Connection—
Do Not Use
- Pin 3—Cathode*
- Pin 5—Plate Number 2
- Pin 7—Plate Number 1

* Opposite ends of the directly heated cathode are connected to pins 1 and 3. Cathode-heating voltage should be connected between these pins. Output current may be taken from either pin 1 or pin 3.

PHYSICAL DIMENSIONS



EIA 12-16

CHARACTERISTICS AND TYPICAL OPERATION

FULL-WAVE RECTIFIER

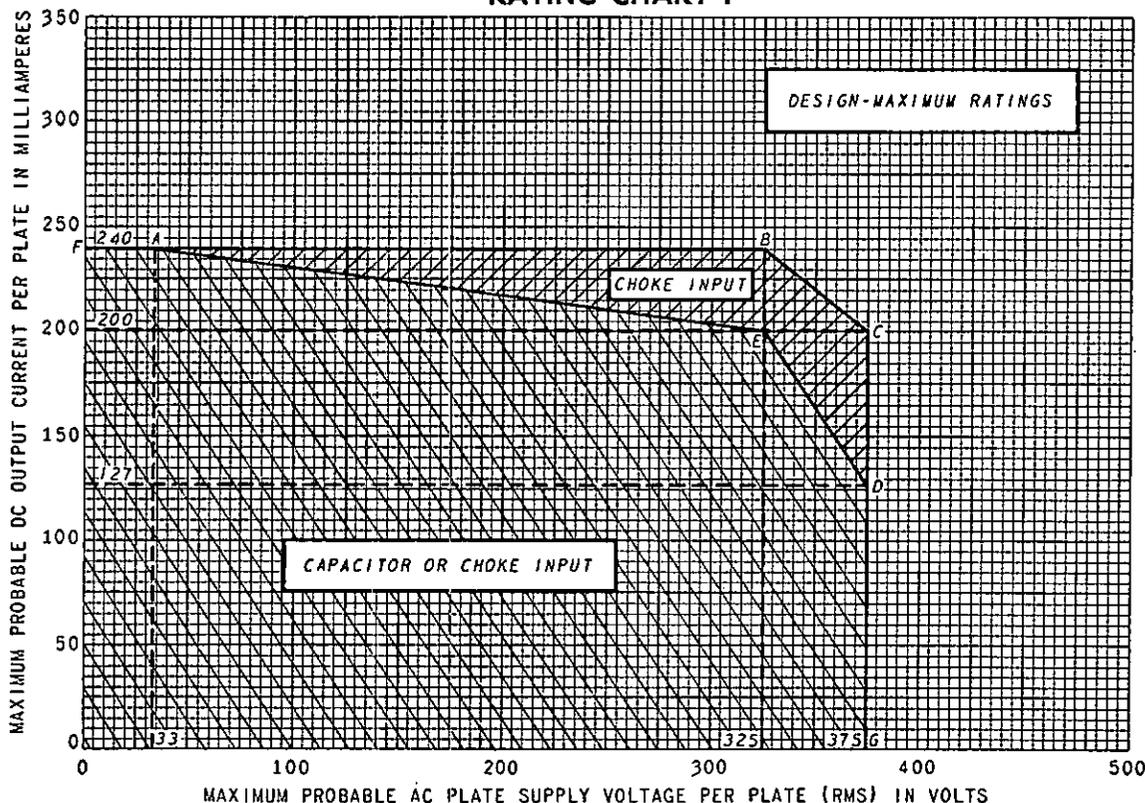
	Capacitor- Input Filter
AC Plate-Supply Voltage per Plate, RMS.	275 Volts
Filter Input Capacitor.	40 Microfarads
Total Plate-Supply Resistance per Plate.	32 Ohms
DC Output Current.	350 Milliamperes
DC Output Voltage at Filter Input.	300 Volts
Tube Voltage Drop	
I _b = 350 Milliamperes DC per Plate.	25 Volts

To simplify the application of the maximum ratings to circuit design, the Design-Maximum ratings are presented in chart form as Rating Charts I, II, and III. Rating Chart I presents the maximum ratings for a-c plate-supply voltage and d-c output current. Rating Chart II provides a convenient method for checking conformance with the maximum steady-state peak-plate-current rating. Rating Chart III offers a convenient method for checking conformance with the maximum transient peak-plate-current rating. Rating Chart I applies to both capacitor-input and choke-input filters, while Rating Charts II and III apply to capacitor-input filters only.

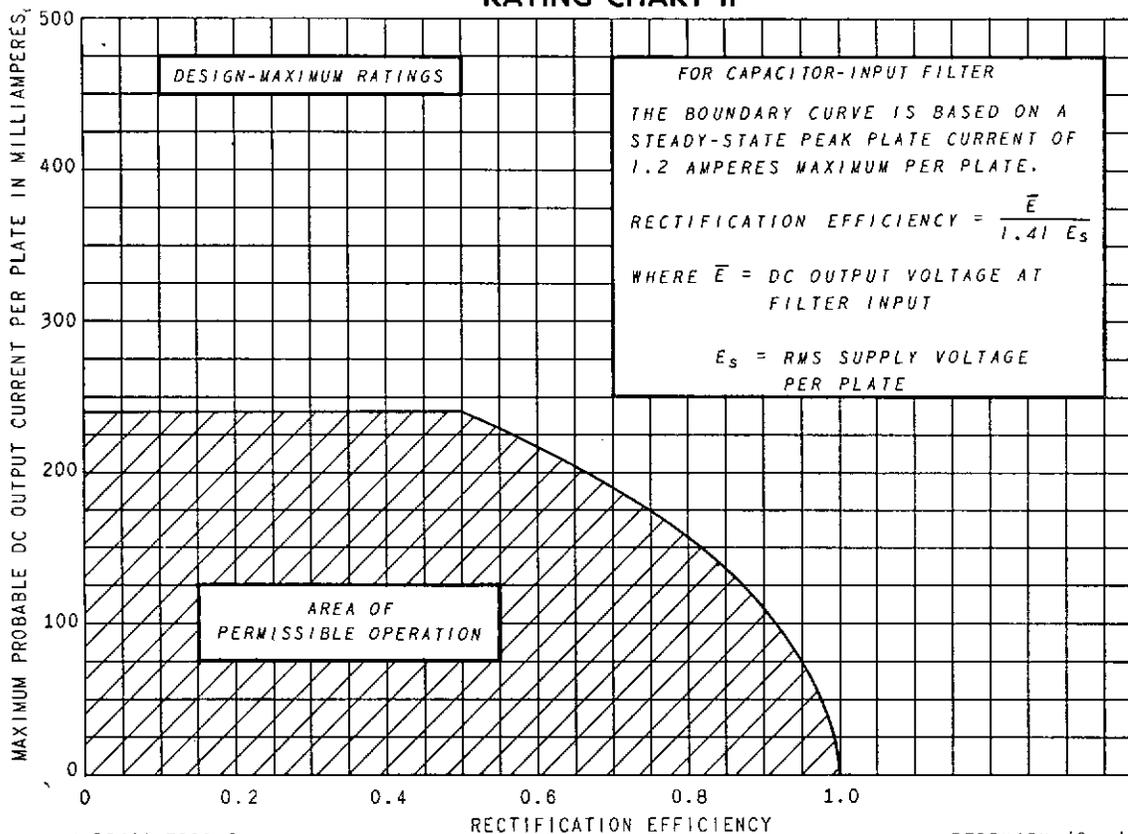
Operating points should be so selected that the boundary limits of a-c plate-supply voltage and d-c output current on Rating Chart I, and maximum d-c output current per plate and rectification efficiency on Rating Chart II, are not exceeded with a bogey tube under the worst probable operating conditions with respect to supply-voltage variation, equipment component variation, equipment control adjustment, and environmental conditions. On Rating Chart I the boundary FAEDG defines the limits for capacitor-input filter operation, and the boundary FABCDG defines the limits for choke-input filter operation.

Rating Chart III shows the minimum value of plate-supply resistance (R_s) required to remain within the transient peak-plate-current rating. The value of R_s should be such that it lies to the left of the line on Rating Chart III at the highest probable value of line voltage.

RATING CHART I



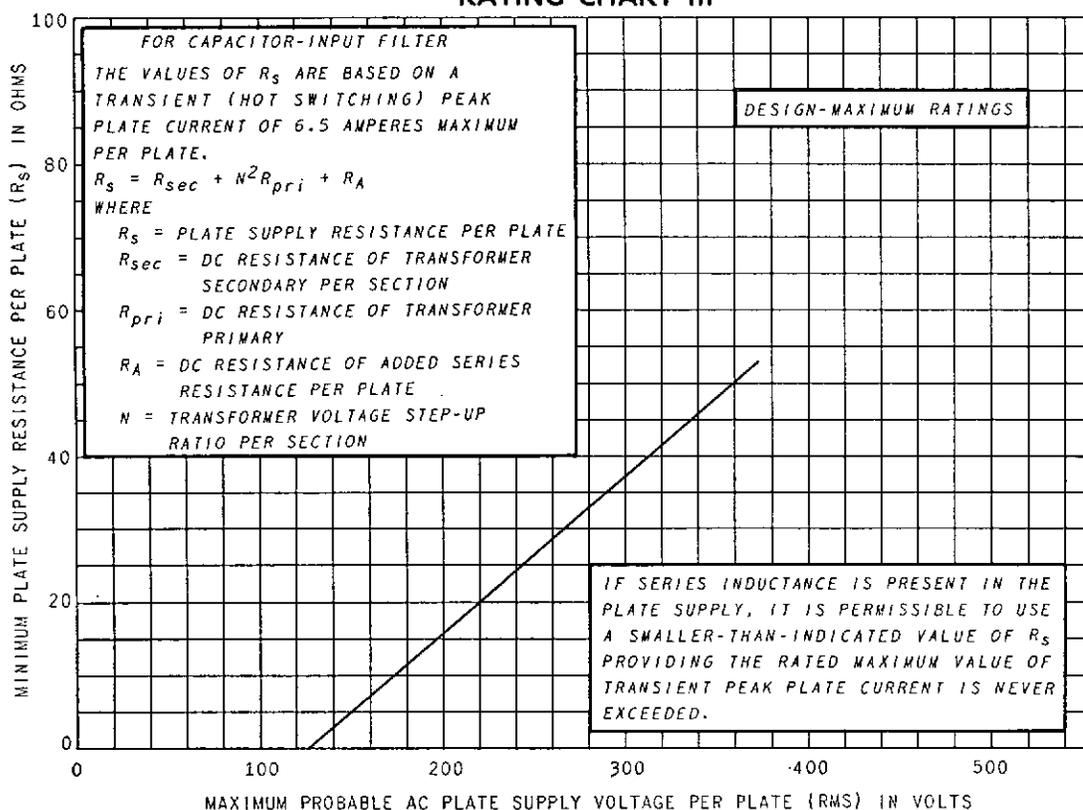
RATING CHART II



K-55611-TD99-2

FEBRUARY 10, 1960

RATING CHART III

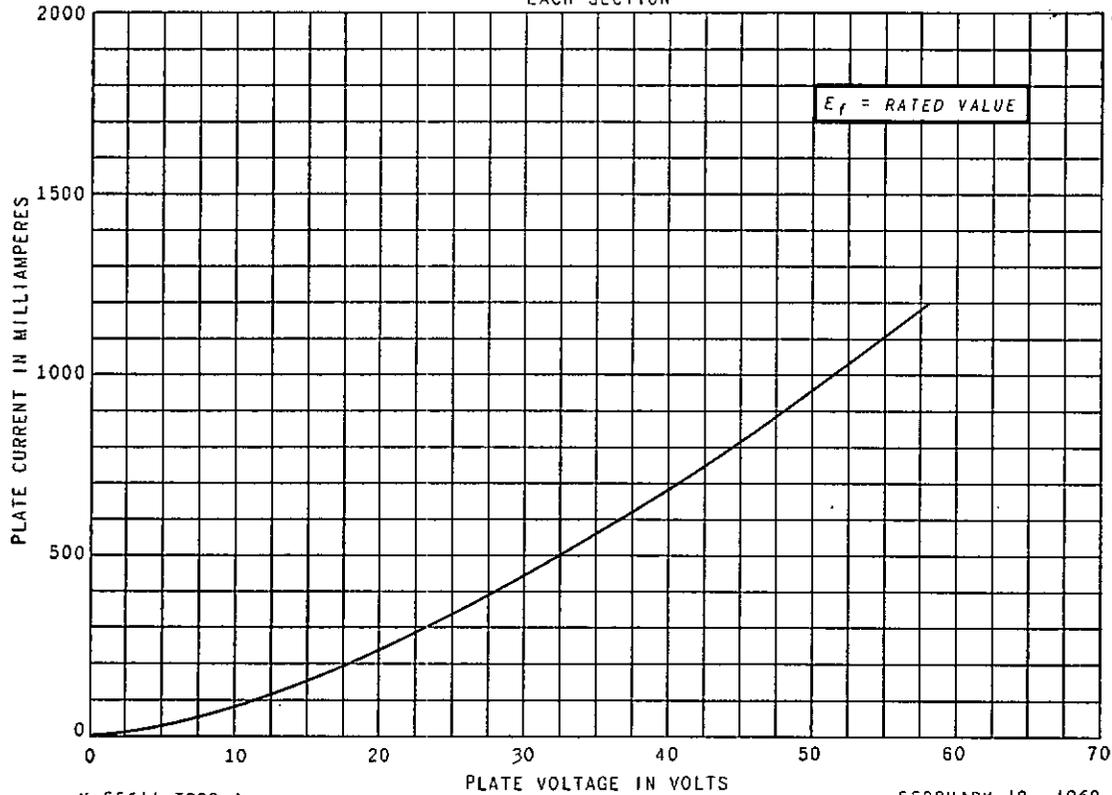


K-55611-TD99-3

FEBRUARY 10, 1960

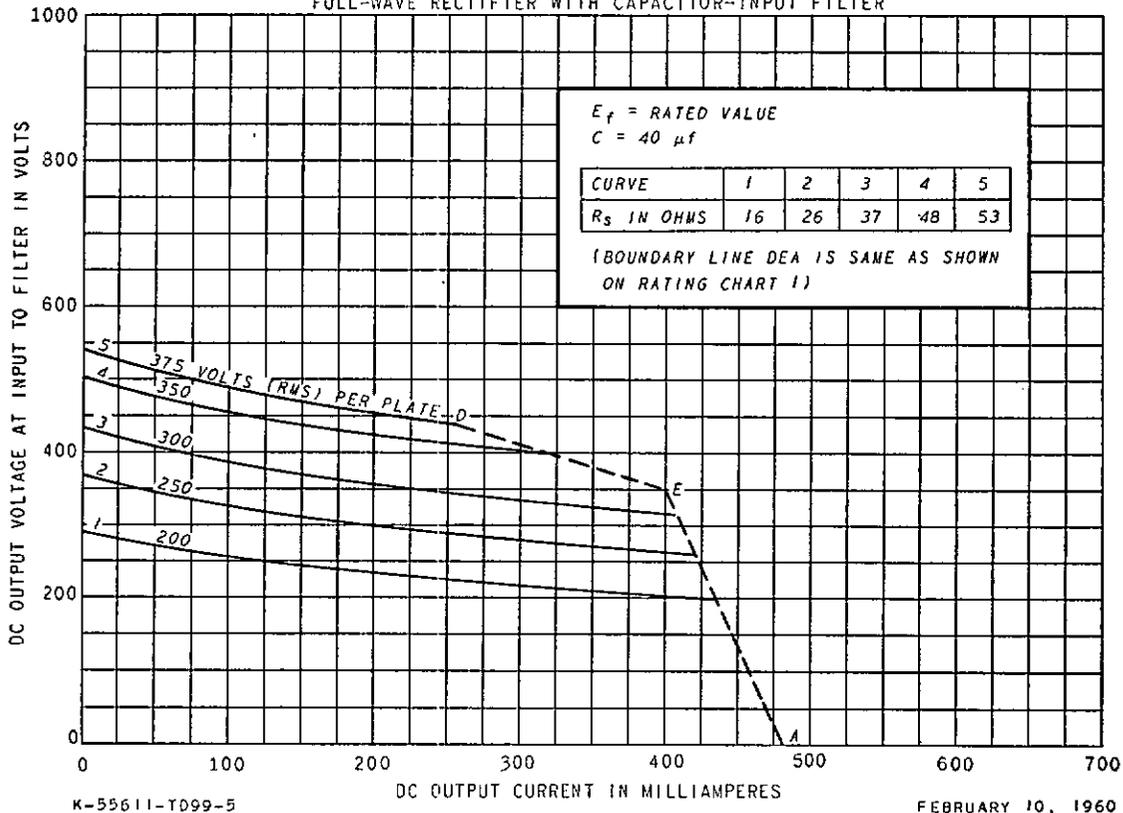
AVERAGE PLATE CHARACTERISTICS

EACH SECTION



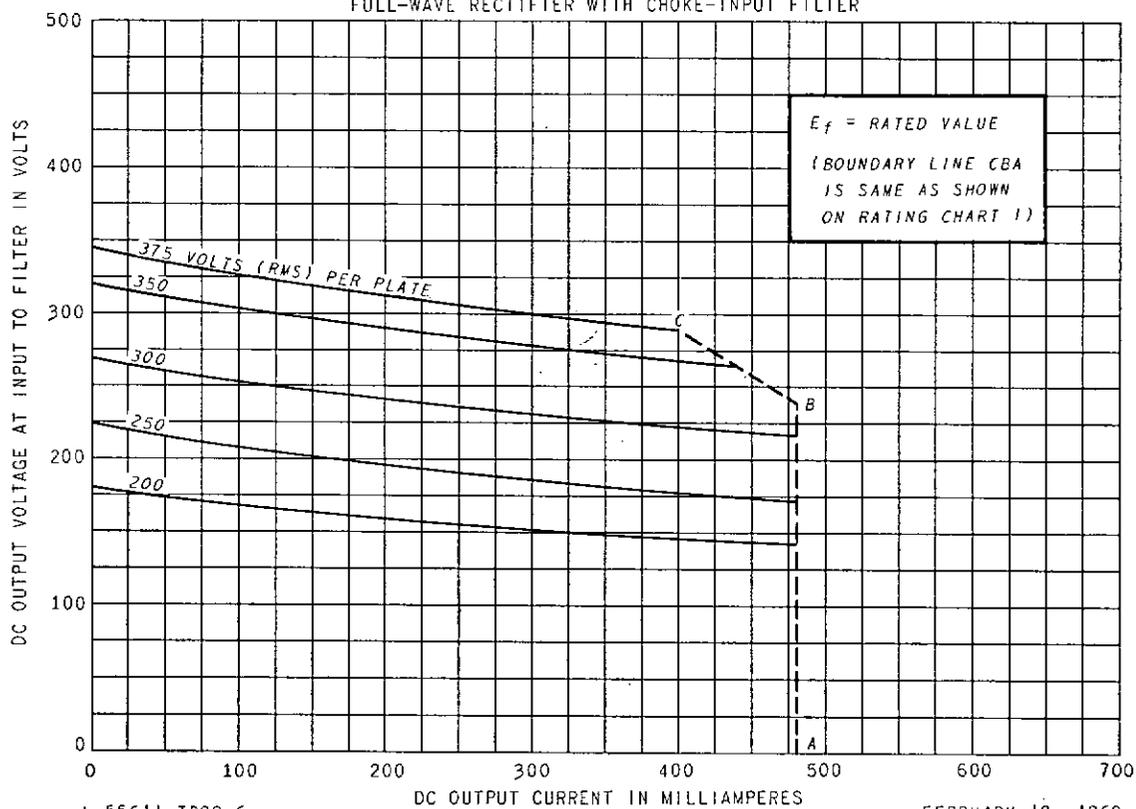
OPERATION CHARACTERISTICS

FULL-WAVE RECTIFIER WITH CAPACITOR-INPUT FILTER



OPERATION CHARACTERISTICS

FULL-WAVE RECTIFIER WITH CHOKE-INPUT FILTER



k-55611-TD99-6

FEBRUARY 10, 1960

ELECTRONIC COMPONENTS DIVISION
GENERAL  ELECTRIC
Schenectady 5, N. Y.